

Fig. 1

Device Capability Table

Node #0	Reception	ATSC-HD/SD, DSS-HD, Analog
	Transmission	None
	Conversion	None
Node #1	Reception	DSS-HD/SD
	Transmission	DSS-HD/SD, Analog
	Conversion	DSS-HD/SD -> Analog
Node #2	Reception	ATSC-HD/SD, DSS-HD/SD, Analog
	Transmission	ATSC-HD/SD, DSS-HD/SD, Analog
	Conversion	None
Node #3	Reception	DV-SD, Analog
	Transmission	DV-SD, Analog
	Conversion	None
Node #4	Reception	DV-SD
	Transmission	ATSC-SD
	Conversion	DV-SD -> ATSC-SD

Fig. 2

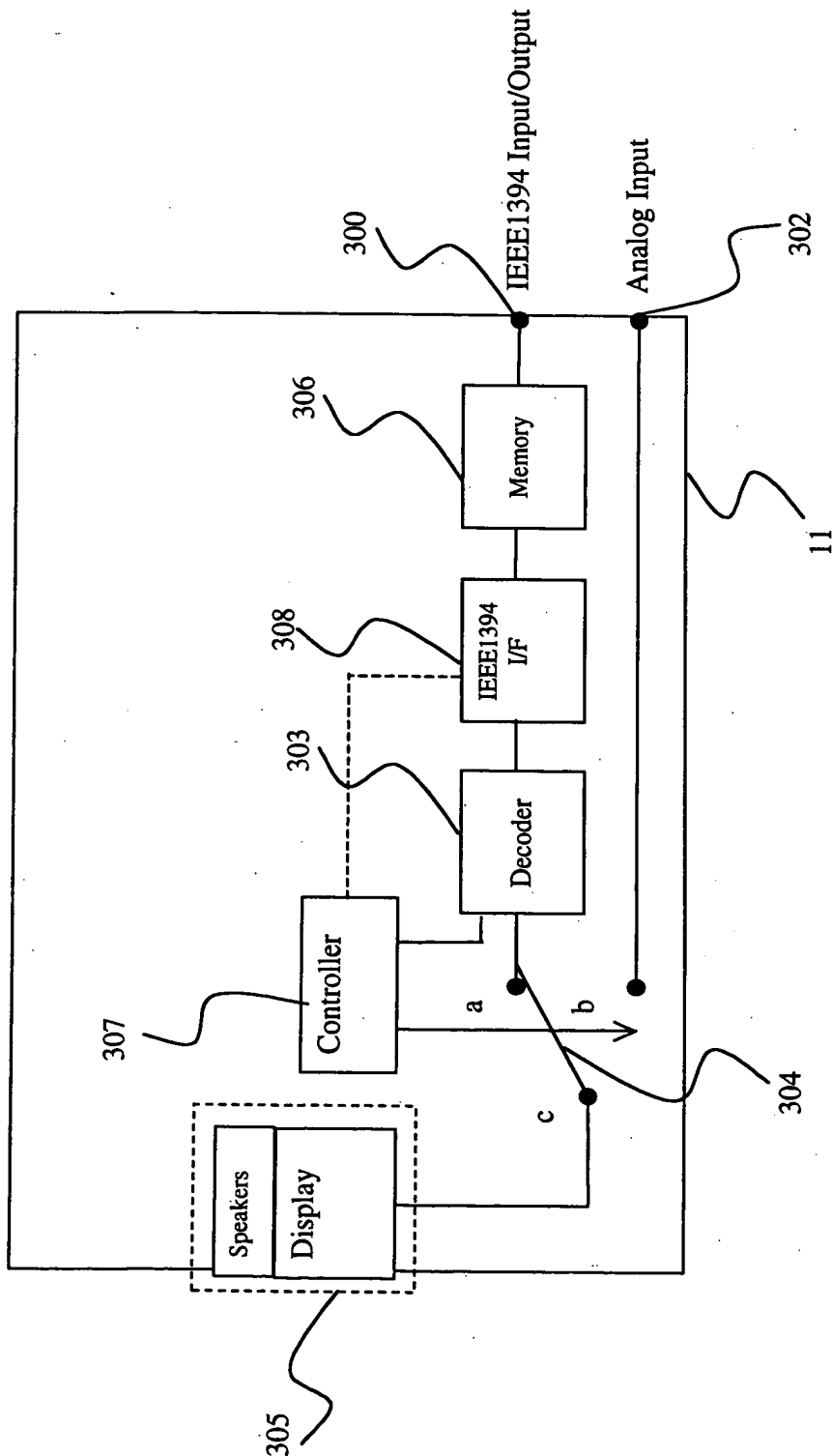
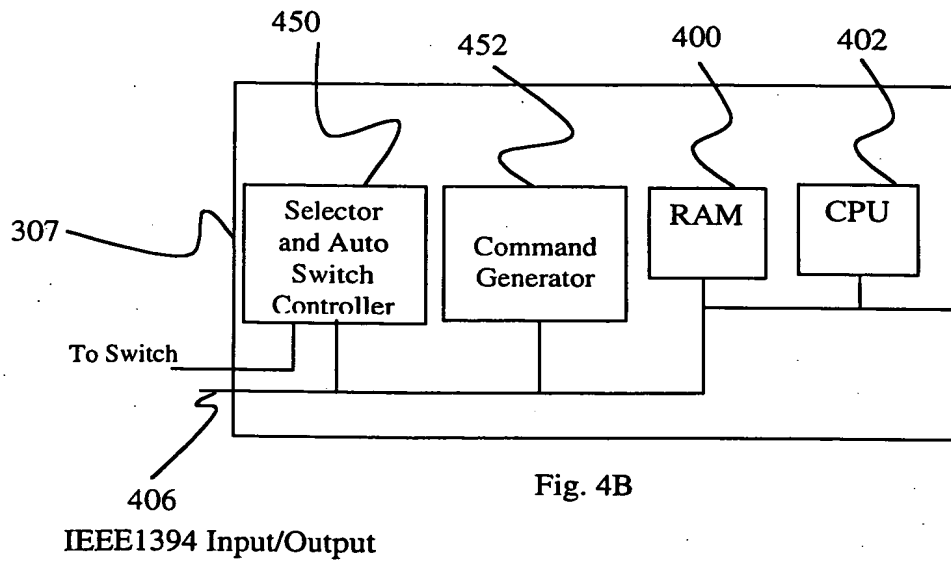
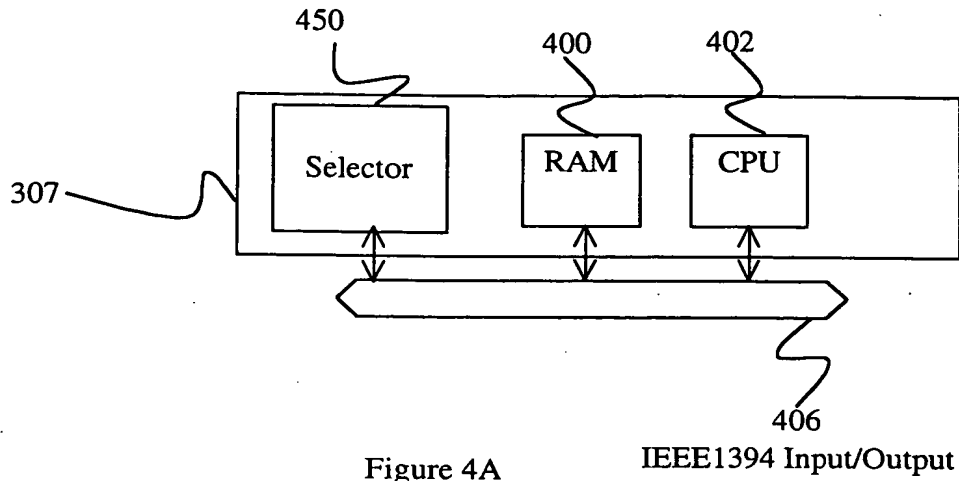


Fig. 3



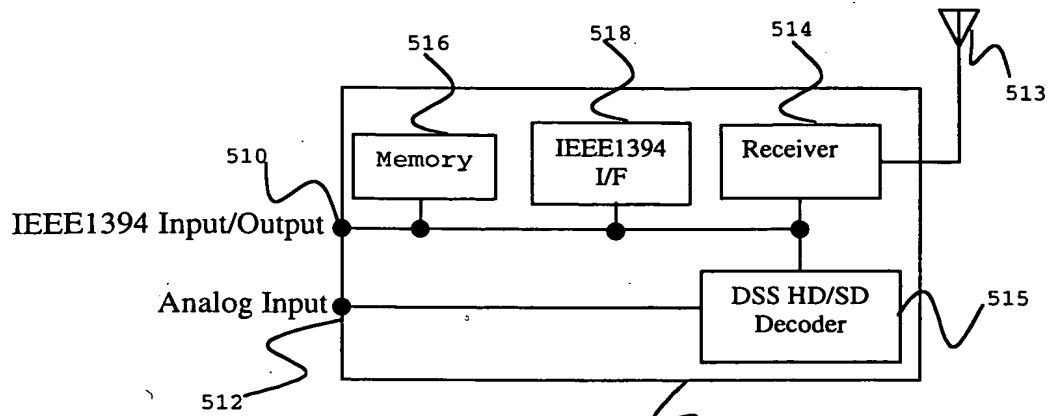


Figure 5

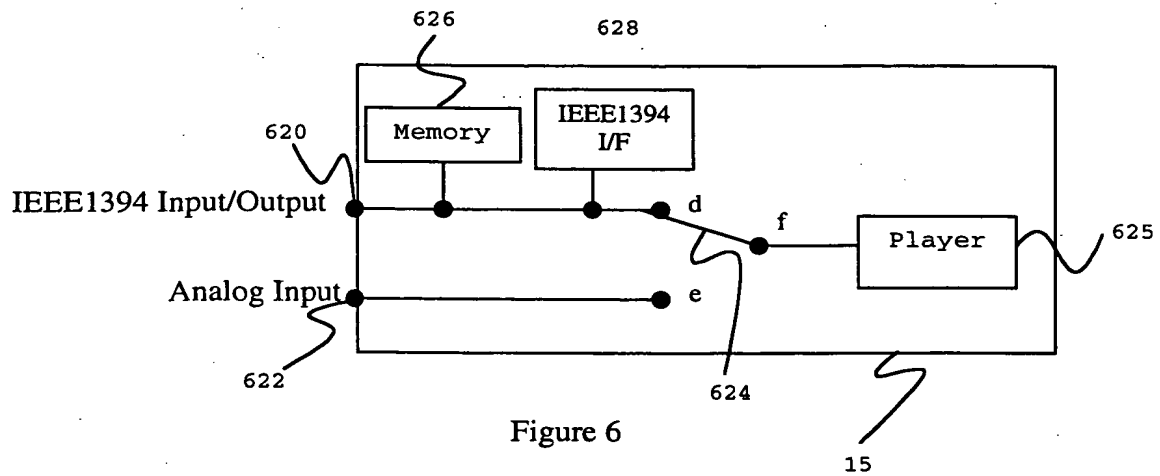


Figure 6

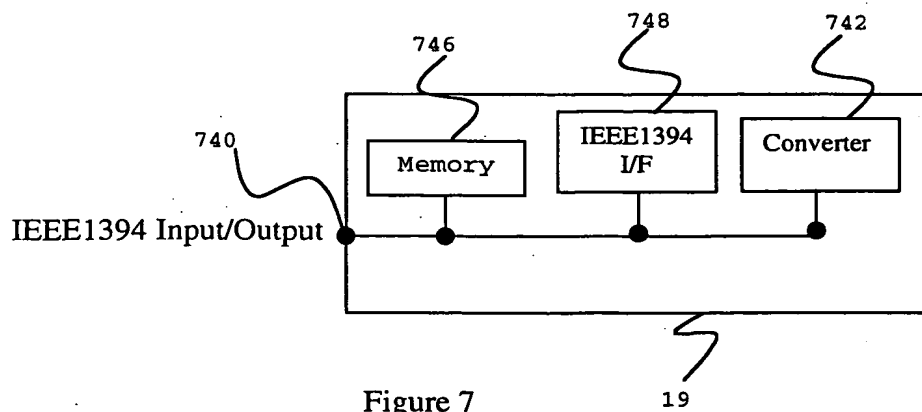


Figure 7

Exemplary Path Table

Source	Format	Route
node #0	N/A	
node #1	DSS-HD	#1 -(Digital) -#0
	DSS-SD	#1 -(Analog) -#0
node #2	ATSC-HD/SD	#2 -(Digital) -#0
	DSS-HD	#2 -(Digital) -#0
	DSS-SD	#2 -(Digital) -#1 -(Analog) -#0
	Analog	#2 -(Analog) -#0
node #3	DV-SD	#3 -(Digital) -#4 -(Digital) -#0
node #4	N/A	

Fig. 8

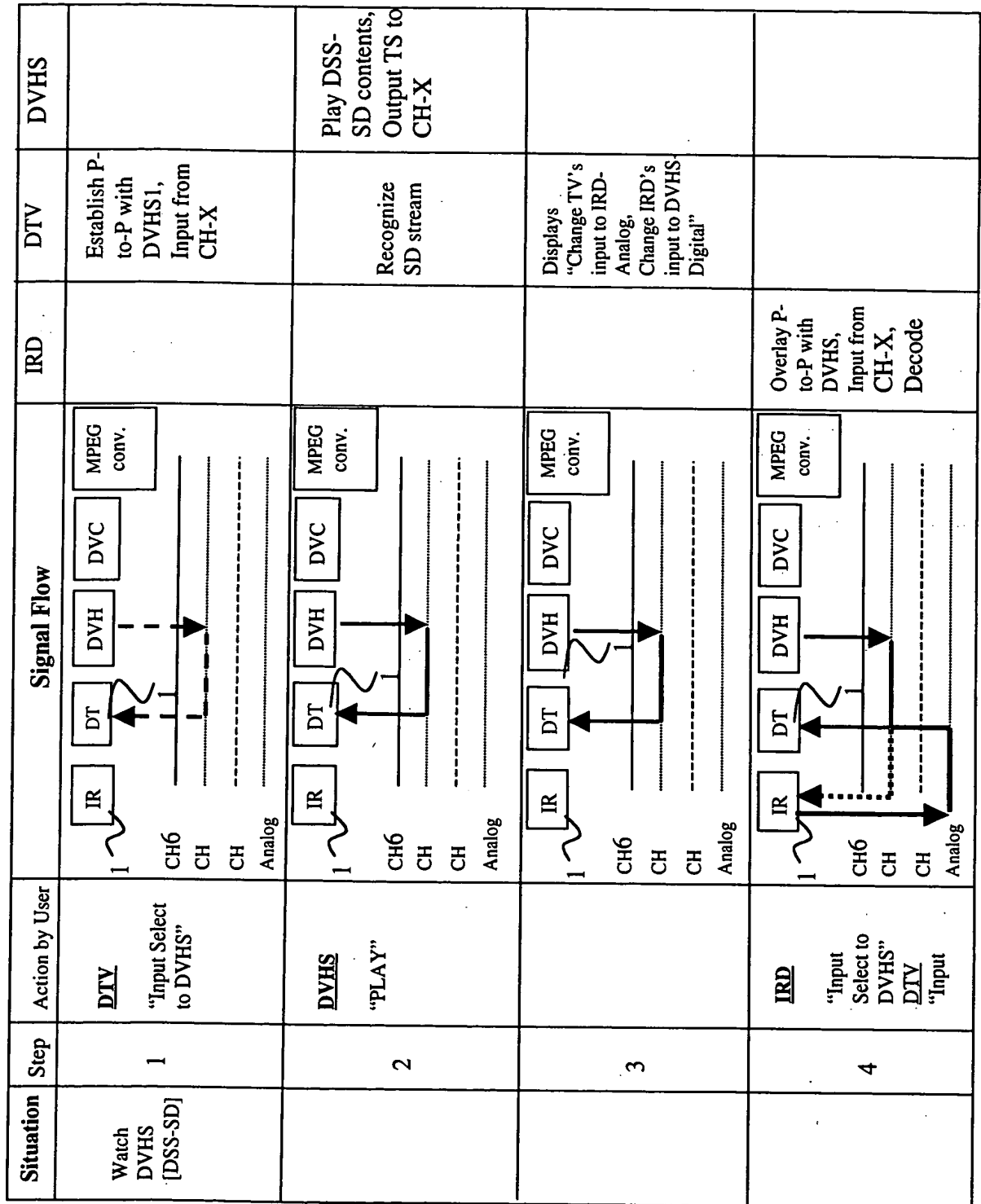


Fig. 9

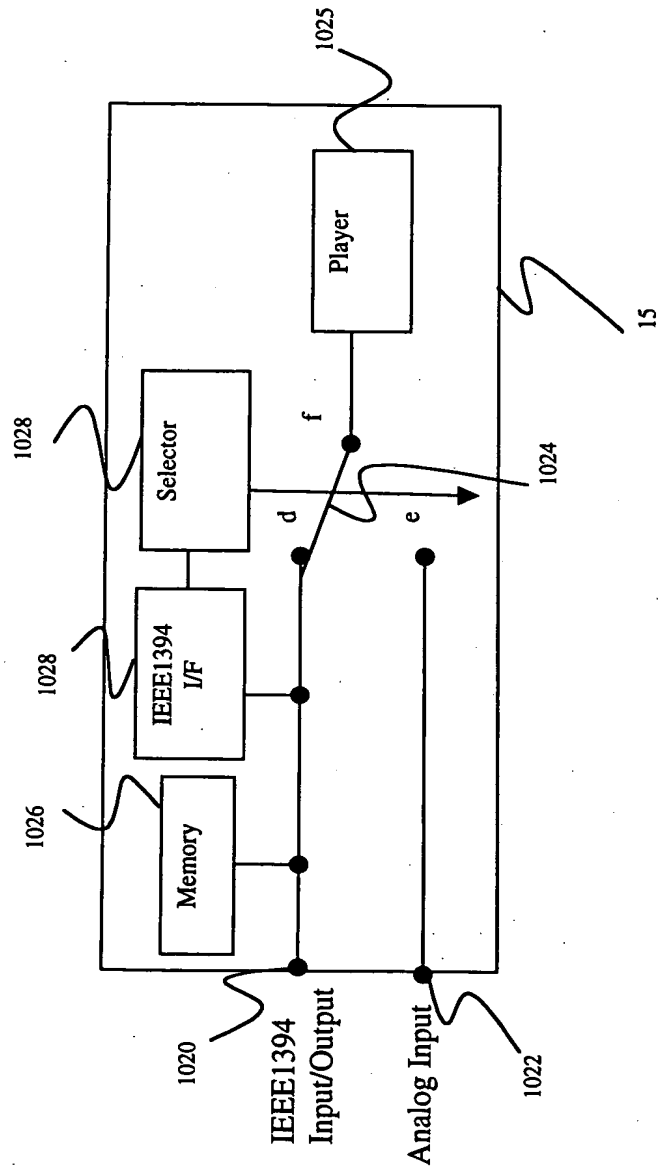


Figure 10